

1 What is claimed is:

2 1. A timepiece structured to determine the elapsed time from a
3 specific event, said timepiece comprising:

4 a) a housing including a display assembly structured to
5 display elapsed time,

6 b) a processor including a chronographic application
7 determinative of the elapsed time,

8 c) an activation assembly cooperatively structured with said
9 processor to selectively instigate monitoring of the
10 elapsed time from the specific event to the current time,
11 and

12 d) said processor further comprising a restrictive
13 designation application structured to at least initially
14 limit selection of the specif event to a single
15 occurrence.

16 2. A timepiece as recited in claim 1 wherein said display
17 assembly is structured to display current elapsed time in at
18 least one time interval.

19 3. A timepiece as recited in claim 2 wherein said display
20 assembly is structured to display current elapsed time in a
21 plurality of different time intervals.

22 4. A timepiece as recited in claim 2 wherein said display
23 assembly is structured to display current elapsed time
24 concurrently in a plurality of different time intervals.

25 5. A timepiece assembly as recited in claim 4 wherein said

1 plurality of time intervals alternatively comprise two
2 different pairs of time intervals.

3 6. A timepiece as recited in claim 1 wherein said restrictive
4 designation application is structured to restrict resetting
5 of said chronographic application.

6 7. A timepiece as recited in claim 6 wherein said processor
7 comprises a memory capability structured to store an original
8 input time of the specific event.

9 8. A timepiece as recited in claim 7 wherein said processor
10 further comprises a limited access application structured to
11 restrict access to said memory capability and any original
12 input time stored therein.

13 9. A timepiece as recited in claim 8 wherein said memory
14 capability is accessed and said original input time stored
15 therein is accessed by defeat of said limited access
16 application.

17 10. A timepiece as recited in claim 9 wherein said chronographic
18 application is responsive to said defeat of said limited
19 access application at least to the extent of being reset.

20 11. A timepiece as recited in claim 10 wherein said chronographic
21 application is operable to determine current elapsed time
22 subsequent to the resetting thereof to a corresponding
23 original input time.

24 12. A timepiece as recited in claim 1 wherein said processor
25 further comprises a memory capability structured to store an

original input time of at least one specific event.

13. A timepiece as recited in claim 12 wherein said memory capability is further structured to store a plurality of original input times, each of said plurality of original input times associated with an occurrence of a different specific event.

14. A timepiece as recited in claim 12 wherein said restrictive designation application is structured to restrict resetting of said chronographic application to said original input time or a new input time subsequent to a predetermined adjustment time period and instigation of time monitoring by said chronographic application.

15. A timepiece as recited in claim 12 wherein said memory capability is operative to automatically restore said original input time to said chronographic application in the event of a power failure.

16. A timepiece structured to determine the elapsed time from at least one specific event, said timepiece comprising:

a) a housing including a display assembly structured to display elapsed time,

b) a processor including a chronographic application determinative of the elapsed time from the specific event to the current time,

c) said processor including a memory capability structured to store at least one original input time of an

- 1 associated specific event,
- 2 d) an activation assembly operative with said chronographic
- 3 application and structured to instigate continuous
- 4 monitoring by said chronographic application of elapsed
- 5 time from the specific event to the current time, and
- 6 e) said processor further comprising a restrictive
- 7 designation application structured to initially limit
- 8 selection of a predetermined number of specific events
- 9 being monitored by said chronographic application.
- 10 17. A timepiece as recited in claim 16 wherein said restrictive
- 11 designation application is structured to restrict resetting of
- 12 said chronographic application.
- 13 18. A timepiece as recited in claim 17 wherein said restrictive
- 14 designation application is structured to restrict resetting of
- 15 said chronographic application by entry of an original input
- 16 time for a specific event other than original input times
- 17 associated with said predetermined numbers specific events.
- 18 19. A timepiece as recited in claim 16 wherein said restrictive
- 19 designation application is structured to restrict entry of
- 20 original input time for a specific event other than original
- 21 input times associated with said predetermined number of
- 22 specific events.
- 23 20. A timepiece as recited in claim 16 wherein said processor
- 24 further comprises a limited access application structured to
- 25 restrict access to said memory capability and an original

input time of an associated specific event stored therein.

21. A timepiece as recited in claim 20 wherein said memory capability is accessed and said original input time is restored to said chronographic application by defeat of said limited access application.

22. A timepiece as recited in claim 16 wherein said display assembly is structured to display current elapsed time concurrently in a plurality of time intervals.

23. A timepiece as recited in claim 16 wherein said memory capability is operative to automatically restore said original input time to said chronographic application in the event of a power failure.

24. A timepiece structured to determine the elapsed time from at least one specific event, said timepiece comprising:

- a) a housing including a display assembly structured to display elapsed time,
- b) a processor including a chronographic application determinative of the elapsed time,
- c) an activation assembly operative with said chronographic application and structured to instigate continuous monitoring by said chronographic application of elapsed time from the specific event to a current time, and
- d) said processor further comprising a restrictive designation application structured to restrict resetting of said chronographic application to any original input

1 time subsequent to instigation of elapsed time monitoring
2 by said chronographic application.

3 25. A timepiece as recited in claim 24 wherein said processor
4 further comprises a memory capability structured to store at
5 least one original input time of an associated specific event.

6 26. A timepiece as recited in claim 25 wherein said processor
7 further comprises a limited access application structured to
8 restrict access to said memory capability and an original
9 input time of an associated specific event stored therein.

10 27. A timepiece as recited in claim 26 wherein said memory
11 capability is accessed and said original input time is
12 restored to said chronographic application by defeat of said
13 limited access application.